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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Present Application:**

Applicant : Paolo Menegoli  
 Title : DMOS TRANSISTORS WITH SCHOTTKY  
           DIODE BODY STRUCTURE  
 Docket No. : 850063.498D2  
 Date : September 12, 2000

**Prior Application:**

Examiner : Michael Trinh  
 Art Unit : 2822  
 Application No. : 09/243,017

Box Patent Application  
 Assistant Commissioner for Patents  
 Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT**

## Assistant Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior Application No. 09/243,017, filed February 3, 1999. The references listed on the attached Form PTO-1449 were submitted to and/or cited by the Patent and Trademark Office in this prior application and, therefore, are not required to be provided in this application. If the Examiner wishes, copies will be provided upon request. As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,  
Paolo Menegoli  
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ERT:vs

Enclosures:

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